U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

Phund My Chung

Art Unit: 2138

Primary Patent Examiner

Applic. No.: 10/631,356 Amdt. Dated February 2, 2006

Reply to Office action of November 2, 2005

Replacement Sheet

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FIG 1 Prior Art

Clock	Command (Bank A)
1	act-A-adr 1
2	0
3	rd-A-adr 1
4	0
5	0
6	0
7	pre-A-adr 1
8	0
9	act-A-adr 2
10	0
11	rd-A-adr 2
12	0
13	0
14	0
15	pre-A-adr 2
16	0

FIG 2 Prior Art

Clock	Command
1	act-A
2	pre-B
3	rd-A
4	act-B
5	pre-C
6	rd-B
7	act-C
8	pre-D
9	rd-C
10	act-D
11	pre-A
12	rd-D

ord = {act, pre, rd, wr} $X = \{A, B, C, D\}$

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PAGE 15/23 * RCVD AT 2/2/2006 12:58:33 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/24 * DNIS:2738300 * CSID:+9549251101 * DURATION (mm-ss):03-50